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APPLICANTS

Chang-Ho Lee, Suwon-si, KOREA, REPUBLIC OF;
 Jong-Hyun Choi, Suwon-si, KOREA, REPUBLIC OF;

** CONTINUING DATA *****

None *AS*

** FOREIGN APPLICATIONS *****

REPUBLIC OF KOREA 03-39163 06/17/2003

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35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance	Examiner's Signature <i>Mills</i> Initials <i>AS</i>			
Verified and Acknowledged <i>Mills</i> <i>AS</i>				

ADDRESS

Steven M. Mills
 MILLS & ONELLO LLP
 Suite 605
 Eleven Beacon Street
 Boston, MA02108

TITLE

Apparatus and method for signal bus line layout in semiconductor device

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